



Package Code 2

<p>Tolerances Except as Noted .x = +/- .05 .xx = +/- .01 .xxx = +/- .005 Dimensions in inches</p>	Revisions				<u>Impellimax</u>		
	A	ECO 877	10/3/89	P.C.	OUTLINE		
	B	ECO 1553	9/15/94	P.C.			
C	ECO 2133	2/15/01	P.C.			Sheet 1 of 2	
<p>Information herein is believed accurate. Suitability not guaranteed.</p>					<p>Drawn By: J.P.</p>	8/16/88	Drawing #
					<p>DRF: -</p>	<p>Approved: P. C.</p>	GS1-50

PIN	CONNECTION	PIN	CONNECTION
1	VEE	14	N C
2	Inv Output	13	N C
3	Ground	12	R set
4	Input	11	N C
5	N C	10	- V internal
6	NI Output	9	N C
7	+ 5 V	8	N C

Notes:

- 1) Input can be driven by TTL logic.
- 2) With logic hi applied, inverting output switches to negative output while noninverting output switches to near ground. Outputs reverse states for logic low input.
- 3) Switching speed (open circuit) is 30 nsec typical.
- 4) Unit contains internal .01 bypass caps on supplies.
- 5) - V can be in the range of - 5 to - 12 Volts (A Code) or - 5 to - 15 Volts (B Code). Connect Rset, in the range of 220 ohms to 4.7K typical, between Rset pin and Vee to set output negative amplitude for optimal GaAs switching voltage. If negative supply is - 5V, connect to - V internal.

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