



<p>Tolerances Except as Noted .x = +/- .05 .xx = +/- .01 .xxx = +/- .005 Dimensions in inches</p>	Revisions				<u>Impellimax</u>	
	A	ECO 2075	8/31/00	P.C.	OUTLINE	
					Not to scale	Sheet 1 of 2
<p>Information herein is believed accurate. Suitability not guaranteed.</p>					Drawn By: PC	Date: 11/14/90
					DRF: -	Approved: P.C.
						Drawing # DS5-50

PIN	CONNECTION	PIN	CONNECTION
1	+ 5 V	22	Testpoint 0
2	A	21	Output 0/5
3	B	20	Output 1
4	C	19	Output 2
5	E	18	Input 0/5
6	Ground	17	Output 3
7	NC	16	NC
8	NC	15	Output 4
9	Zen	14	NC
10	NC	13	Testpoint 5
11	Vee	12	NC

Notes:

- 1) Inputs are direct connection to 54LS138 IC, with 4K nominal pull-ups. E input must be hi for decoding operation. Connect Input 0/5 to Testpoint 0 to allow decoding from 000, or connect Input 0/5 to Testpoint 7 to allow decoding beginning from 001.
- 2) Switching speed is 40 nsec typical into resistive loads. Outputs are not spiked.
- 3) Negative supply range is -2 to -12 V for Logic Code "A", or -2 to -15V for Logic Code "B". Negative output current increases with increasing negative voltage.
- 4) Unit is reverse bias protected and contains internal .01 uF bypass capacitors on both power supplies.
- 5) Quiescent current is +45 mA / - 10 mA typical.

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