



<p>Tolerances Except as Noted</p> <p>.x = +/- .05 .xx = +/- .01 .xxx = +/- .005</p> <p>Dimensions in inches</p>	Revisions				Impellimax			
	Rev	Number	Date	By	OUTLINE			
	A	ECO 2115	1/11/01	PC				
					Drawn By:		Date:	
				RWW		1/11/01		
<p>Information herein is believed accurate. Suitability not guaranteed.</p>					DRF:	Date:	Approved:	Drawing # DF5-52
					232	11/21/98	PC	

PIN	CONNECTION	PIN	CONNECTION
1	A	22	Logic 0
2	B	21	Logic 5
3	C	20	Input 0/5
4	Enable Low	19	Testpoint 0/5
5	Enable Hi	18	Output 0/5
6	Ground	17	Output 1
7	Testpoint 4	16	Testpoint 1
8	Output 4	15	Output 2
9	Testpoint 3	14	Testpoint 2
10	Output 3	13	NC
11	-5V to -50V	12	+5V

Notes:

- 1) Enable Low must be low, and Enable Hi must be hi for decoding operation. If either enable is not satisfied, all outputs are forced positive.
- 2) Selected output goes negative, all others remain positive.
- 3) Switching speed is 30 nsec open circuit, 100 nsec typical with a high power diode load.
- 4) Outputs provide current spikes.
- 5) Inputs are direct connection to 54F138 IC. A is least significant bit. Connect input 0/5 to logic 0 for 0 thru 4 decoding, or connect input 0/5 to logic 5 for 1 thru 5 decoding.
- 6) Unit contains .01 uF bypassing on positive supply, and 5000 pF bypassing on negative supply. Additional external power supply bypassing is recommended for best switching speed performance.

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