



Package Code 2

<p>Tolerances Except as Noted .x = +/- .05 .xx = +/- .01 .xxx = +/- .005 Dimensions in inches</p>	<b>Revisions</b>				<b><u>Impellimax</u></b>		
	A	ECO 2048	6/7/00	P.C.	<b>OUTLINE</b>		
<p>Information herein is believed accurate. Suitability not guaranteed.</p>						Sheet 1 of 2	
					<p>Drawn By: P.C.</p> <p>DRF: 183</p>	<p>Date: 9/8/98</p> <p>Approved: P. C.</p>	<p>Drawing #</p> <p><b>APD7-50</b></p>

PIN	CONNECTION	PIN	CONNECTION
1	-V	14	BP 1
2	Output A	13	BP 2
3	Ground	12	BP 3
4	Input	11	BP 4
5	Compensation	10	BP 5
6	Output B	9	BP 6
7	+ V	8	BP 7

**Notes:**

- 1) Input impedance is 15 K nominal
- 2) Unit contains internal .01 bypass capacitors on both supplies.
- 3) Output A and Output B source matched positive currents. The second output can be used to provide tailored spiking (see application notes) or to drive DC-isolated multiple diodes. For tailored spiking, load the second output with a resistor to ground and connect a small value capacitor or RC network between Output A and Output B. Component values are application-specific for best performance.
- 4) Total delta between supply voltages should be 18V or less ("A" voltage code).
- 5) Uncompensated settling time into resistive loads is 100 nsec.
- 6) Outputs provide positive current concave-up transfer function.

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