



Package Code 2, thin

<p>Tolerances Except as Noted .x = +/- .05 .xx = +/- .01 .xxx = +/- .005 Dimensions in inches</p>	Revisions				<u>Impellimax</u>		
	A	ECO 1986	2/4/00	PC	OUTLINE		
	B	ECO 2163	5/2/01	PC			
<p>Information herein is believed accurate. Suitability not guaranteed.</p>						Sheet 1 of 2	
					<p>Drawn By: PC</p> <p>DRF: 422</p>	<p>Date: 1/27/00</p> <p>Approved: PC</p>	<p>Drawing #</p> <p>9784-50</p>

PIN	CONNECTION	PIN	CONNECTION
1	+ 5V	14	Switch 1-2 Output
2	- 5V	13	Switch 1-1 Output
3	NC	12	Amp 1 Output
4	Switch 2-2 Output	11	Amp 2 Output
5	A0 Input	10	Switch 2-1 Output
6	Ground	9	NC
7	A1 input	8	DRO

Notes :

- 1) Logic inputs A0 and A1 are compatible with CMOS outputs and require pull-ups if operated with bipolar TTL outputs.
- 2) Switch outputs swing between +4.5V and -4.5V minimum. Output current, in the range of 1 to 15 mA, is to be set by external series resistors. Amplifier and DRO outputs are not current limited and do not actively pull down to ground.
- 3) Switching speed is 4 microseconds typical, 8 microseconds maximum.
- 4) Unit does not contain internal .01 uF bypass capacitors on either power supply.
- 5) Supply current, exclusive of load current, is +/- 10 mA max when operated at +/- 5V at low PRF.

Band	Control Line		Switch 1-1		Switch 1-2		Switch 2-1		Amp 1 U1		AMP 2 U2		Switch 2-2		DRO	
Connection point	5	3	A		D		F		B		E		H		C	
Voltage/Current	A1	A0	Voltage (V)	Current (mA)	Voltage (V)	Current (mA)	Voltage (V)	Current (mA)	Voltage (V)	Current (mA)	Voltage (V)	Current (mA)	Voltage (V)	Current (mA)	Voltage (V)	Current (mA)
X Band path	0	0	+5	+10	-5	-10	+5	+10	+5	+24	0	0	-5	-10	0	0
K Band path (down converter)	0	1	-5	-10	+5	+10	+5	+10	+5	+24	+5	+24	+5	+10	+5	+25
S Band path (up converter)	1	0	+5	+10	-5	-10	-5	-10	+5	+24	0	0	+5	+10	+5	+25
X Band test	1	1	+5	+10	+5	+10	+5	+10	0	0	0	0	+5	+10	0	0

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