



Package Code 2

<p>Tolerances Except as Noted .x = +/- .05 .xx = +/- .01 .xxx = +/- .005 Dimensions in inches</p>	Revisions				<u>Impellimax</u>		
					OUTLINE		
							Sheet 1 of 2
<p>Information herein is believed accurate. Suitability not guaranteed.</p>					Drawn By: P.C.	Date: 6/7/00	<p>Drawing # 8052-50</p>
					DRF: 477	Approved: P. C.	

PIN	CONNECTION	PIN	CONNECTION
1	-V	14	BP 1
2	Output A	13	BP 2
3	Ground	12	BP 3
4	Input	11	BP 4
5	Compensation	10	BP 5
6	Output B	9	Down BP 1
7	+ V	8	Down BP 2

Notes:

- 1) Input impedance is 1.75 K nominal
- 2) Unit does not contain internal .01 bypass capacitors on either supply.
- 3) Output A and Output B source matched positive currents. The second output can be used to provide tailored spiking (see application notes) or to drive DC-isolated multiple diodes. For tailored spiking, load the second output with a resistor to ground and connect a small value capacitor or RC network between Output A and Output B. Component values are application-specific for best performance.
- 4) Total delta between supply voltages should be 18V or less ("A" voltage code).
- 5) Uncompensated settling time into resistive loads is 100 nsec.
- 6) Outputs provide positive current concave-up transfer function using BP 1 thru BP 5. Down BP1 and Down BP2 are inverting, causing a reduction in positive output current for an increasing input voltage. This allows "bumps" to be tuned out of nonmonotonic curves. Note that Down BP's operate at gain = -1/2.

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